

AN603: Active Serial Remote System Upgrade Reference Design

Included Files

The following table provides the directory structure of the files in this release:

Directory	Filename	Description
\	Readme.pdf	This file
\AS_RU_Factory	AS_RU_Factory.qar	Quartus II Archive File (.qar) for factory configuration image.
\AS_RU_App	AS_RU_App.qar	Quartus II Archive File (.qar) for application configuration image.
\Programming Files	AS_RU_Factory_App.pof	Programming Object File (.pof) file containing factory configuration image, and application configuration image.
	AS_RU_Factory_App.jic	JTAG Indirect Configuration (.jic) file containing factory configuration image, and application configuration image.
	AS_RU_Factory_App.map	Flash Memory Allocation Mapping (.map) file for 2_pages_factory_app.jic/pof.
\Multiplexer.v	Multiplexer.v	Verilog example code to multiplex the inputs to ALTASMI_PARALLEL. This code controls the access to ALTASMI_PARALLEL between two drivers (ALTREMOTE_UPDATE megafunction and update logic).

Revision History

Version 1.0: Initial published version.

Support

If you have additional questions with the reference design provided, please contact Altera support at:

World Wide Web: <http://www.altera.com>
<http://www.altera.com/mysupport/>

Technical Support Hotline: (800) 800-3753 (U.S.)
(408) 544-7000 (International)

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